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block 410a "DBA03". Furthermore, it is of type programmable system block. Referring to Figure 13A, a ljmp to the counter_16 ISR has been automatically placed in the vector table 1300 at the location for programmable system block DBA03. Referring now to Figure 1B, the ljmp was placed automatically to reflect the configuration having module 304 CNTR16_MSB at digital block 410a "DBA03". Other interrupt vectors have been automatically added to the interrupt vector table 1300, as well.

Please replace the paragraph beginning at page 28, line 9 with the following new paragraph:

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Automatic generation of datasheets is described in co-pending US patent application serial number 09/994,600, filed concurrently herewith, entitled "SYSTEM AND METHOD FOR DYNAMICALLY GENERATING A CONFIGURATION DATASHEET," by Ogami et al., attorney docket number CYPR-CD01174M and assigned to the assignee of the present invention and incorporated herein by reference.

IN THE ABSTRACT

Please replace the Abstract beginning at page 38, line 4 with the following new Abstract:

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A method to facilitate circuit design. First, a schematic and data sheet for a selected module may be displayed. Next, in response to a request for a position for the module among available resources (e.g., programmable system blocks), a potential position for the module is computed. The position is displayed on a graphical user interface by mapping the module to one or more programmable system blocks. Additional user modules may then be selected and placed. After allowing the user to configure the circuit by selecting circuit parameters and pin-outs, various items are automatically generated to facilitate programming the target device. For example, application programming interfaces (APIs) for programming an operation of the modules, source code for